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GGE Gielen, HCC Walscharts, WMC Sansen - Solid-State Circuits, IEEE Journal of, 1990 - [ieeexplore.ieee.org](#)

... for system-level (eventually mixed analog-digital) **simulation**. ... An analog **circuit** is regarded as a multidimensional space ... The valid **design** space for a particular ...

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EB Eichelberger, TW Williams - Proceedings of the 14th **design** automation conference, 1977 - [portal.acm.org](#)

... because of the expected change in the **circuit-to-pin** ... The **design** constraints outlined in Section 4 help solve ... both test generation and fault **simulation** of the ...

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Y Hollander - US Patent 6,530,054, 2003 - Google Patents

... for functionally verifying an integrated **circuit design**. ... The invention is platform and **simulator-independent**, and is ... interaction with any **simulator** through a ...

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... **simulation** method for electronic **circuit design** integrated with ... concurrent with the functional **design** of a ... manufacturing and test **simulator** (MTSIM) simulates ...

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CE Molnar, TP Fang, FU Rosenberger - 1985 Chapel Hill Conference on Very Large Scale Integration, 1985 - Computer Science Press

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[Transition density: a new measure of activity in digital circuits - all 8 versions »](#)

FN Najm, TIS Process, D Center, TX Dallas - Computer-Aided **Design** of Integrated Circuits and Systems, ..., 1993 - [ieeexplore.ieee.org](#)

... implemented in a prototype density **simulator**, called DENSIM. ... by traditional means, the **circuit** must be ... IEEE TRANSACTIONS ON COMPUTER-AIDED **DESIGN** OF INTEGRATED ...

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TL Quarles - 1989 - [eecs.berkeley.edu](#)

... 1.6 : Integration of **Modules** into a Whole ... past quarter century, **circuit simulation**


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GGE Gielen, HCC Walscharts, WMC Sansen - Solid-State Circuits, IEEE Journal of, 1989 - ieeexplore.ieee.org

... a symbolic **simulator** is a valuable analog **design** aid and ... between symbolic and numerical **simulation** are now ... although it is a simple two-transistor **circuit**. ...

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GR Lawman, RW Wells - US Patent 5,673,198, 1997 - Google Patents

... For implementation has been developed, **simulation** of the ... 134. Similarly, feedback is packaged as an API message **circuit** on the **design** window 11. ...

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Hardware design of modular robotic system - all 2 versions »

S Murata, E Yoshida, K Tomita, H Kurokawa, A ... - Intelligent Robots and Systems, 2000. (IROS 2000). ... , 2000 - ieeexplore.ieee.org

... Simplicity is also required in **circuit design**. ... As the first step, we have built a **simulator** for the ... **GUI**) based on Open GL to assist the **design** process of the ...

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DL Allen, LJ Cooper, GL Frenkil, TJ Miller - US Patent 6,151,568, 2000 - Google Patents

... the power consumed by the electrical **circuit**. ... a micro-synthesis **module** 70, a **simulator**

module 74 and ... structure of the semiconductor chip **design** as described ...

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Vendor-neutral integrated vehicle electrical design and analysis system and method - all 2 versions »

JAJ Jolliffe, M Vedapudi, SS Devlin, RJ Louch - US Patent 5,646,862, 1997 - Google Patents

... internal 'data structures are translated into a 26 line loading 28, **circuit simulation** 30, FMEA+ ... 20 **design** 22> digital **simulation** 24, EMC **design** rule checker ...

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A CAD suite for high-performance FPGA design - all 13 versions »

B Hutchings, P Bellows, J Hawkins, S Hemmert, B ... - Field-Programmable Custom Computing Machines, 1999. FCCM'99. ... , 1999 - ieeexplore.ieee.org

... The **simulation** and development environments leverage the basic JHDL ... is shown in Figure

9. This simple **design** instances the adder **circuit** which has ...


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 TG Richards, EJ Hughes, DG Tilley - Multimedia Computing and Systems, 1999. IEEE International ..., 1999 - [ieeexplore.ieee.org](#)

 ... video material was tested using the JMF sample **Player** applet written ... 2-D **Circuit Design** MM5 (see ... gained developing the 2-D actuator **simulation** MM2 highlighted ...

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[A simulation tool for analysis of alternative paradigms for the newelectricity business - all 4 versions »](#)

 TJ Overbye, PW Sauer, G Gross, MJ Laufenberg, JD ... - System Sciences, 1997, Proceedings of the Thirtieth Hawaii ..., 1997 - [ieeexplore.ieee.org](#)

 ... the left mouse button on the **circuit** breaker to ... case, script file, and main one-line diagram information from ... 2. While **simulation** time <= end time Do a. Change ...

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 F Loop, MB This, D Ideas - [email.ednmag.com](#)

 ... A traditional loudness **circuit** compensates for changes in ... is connected and presents the block diagram appropriate to ... also operates in a **simulation** mode without ...

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 R Host, C Microprocessors - [mepcore.com](#)

 ... linked to the **simulator** when the **simulator** is customized ... Figure 7 is a block diagram of the DVD **player** ... control and a back-end processing **circuit** implemented by ...

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 D DARPA - [gigascale.org](#)

 ... embedding specific CAD optimizations of **circuit** topology/embedding ... **design** of system with studies, **design** of system ... engine, **GUI** engine, **GUI** codesign codesign ...

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[A web-based simulation game as a learning tool for the design process of complex systems - all 4 versions »](#)

 F Bianconi, SA Saetta, L Tiacci - Journal of **Design** Research, 2006 - [Inderscience](#)

 ... table and see the resulting logic **circuit** that would ... Evaluation of the **simulation** game set inquires the usability of ... be used for supporting the **design** process. ...

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[Visualizing power system operations in an open market - all 4 versions »](#)

 TJ Overbye, G Gross, MJ Laufenberg, PW Sauer - Computer Applications in Power, IEEE, 1997 - [ieeexplore.ieee.org](#)

 ... The user controls the **simulation** by clicking the mouse on different components on the one-line diagram. For example, the status of **circuit** breakers can ...

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10258	(circuit and simulation).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/26 07:40
L2	94	I1 and ((independent autonomous) same block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/26 07:41
L3	77	I1 and GUI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/26 07:41
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L9	124	I8 and schematic and model	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/26 07:42
L10	7	I9 and GUI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/26 07:42

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